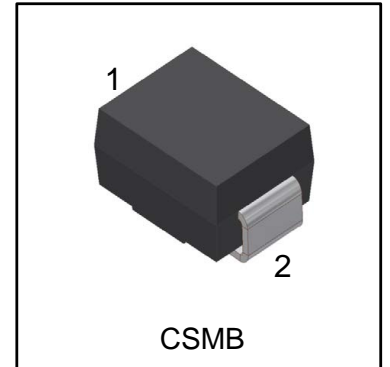


CLP0080SC thru CLP5000SC S-CLP0080SC thru S-CLP5000SC

Transient Suppressor Protection Device

FEATURES and Benefits

- Low voltage overshoot
- Low on-state voltage
 - Does not degrade surge capability after multiple surge events within limit
- Fails short circuit when surged in excess of ratings
- Low capacitance
- Low inductance
- High temperature soldering guaranteed: 260°C/10 seconds.
- Weight: 0.11g
- We declare that the material of product complies with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.



MECHANICAL DATA

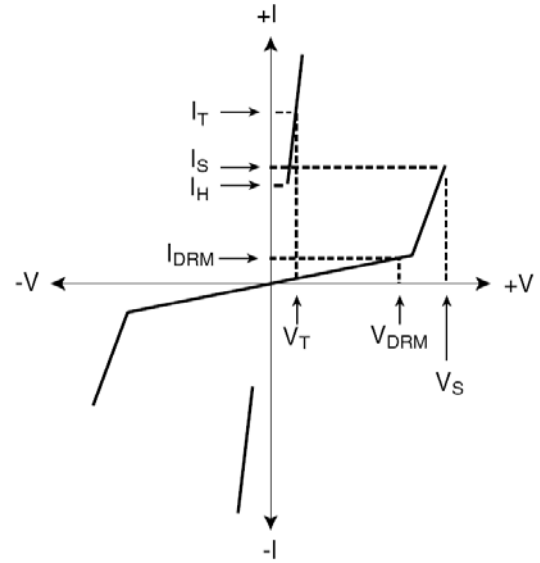
Case: JEDEC DO-214AA molded plastic

Applicable Global Standards

- TIA-968-A
- ITU K.20/21 Enhanced level
- ITU K.20/21 Basic level
- GR 1089 inter building
- IEC 6100-4-5
- YD/T 1082 ; YD/T 993 ; YD/T 950

1. Electrical Parameters

Parameter	Definition
C_o	Off-state Capacitance - typical capacitance measured in off state
I_s	Switching Current - maximum current required to switch to on state
I_{DRM}	Leakage Current - maximum peak off-state current measured at V_{DRM}
I_H	Holding Current - minimum current required to maintain on state
I_{PP}	Peak Pulse Current - maximum rated peak impulse current
I_T	On-state Current - maximum rated continuous on-state current
V_s	Switching Voltage - maximum voltage prior to switching to on state
V_{DRM}	Peak Off-state Voltage - maximum voltage that can be applied while maintaining off state
V_T	On-state Voltage - maximum voltage measured at rated on-state current
V_{PP}	Peak Pulse Voltage - maximum rated peak impulse voltage



2. ELECTRICAL CHARACTERISTICS CURVES

CLP0080SC - CLP5000SC Series - DO-214AA (CSMB) @10/700 μ s, 4KV

Part Number	Marking	V_{DRM}	V_s	V_T	I_s	I_T	I_H	Capacitance	
		@ $I_{DRM}=5\mu A$	@100V/ μ s	@ $I_T=2.2A$				@1MHz, 2V bias	
		V min	V max	V max	mA max	A max	mA min	pF min	pF max
CLP0080SC/S-CLP0080SC	P-8C	6	25	4	800	2.2	50	25	150
CLP0300SC/S-CLP0300SC	P03C	25	40	4	800	2.2	50	15	140
CLP0640SC/S-CLP0640SC	P06C	58	77	4	800	2.2	150	40	60
CLP0720SC/S-CLP0720SC	P07C	65	88	4	800	2.2	150	35	60
CLP0900SC/S-CLP0900SC	P09C	75	98	4	800	2.2	150	25	55
CLP1100SC/S-CLP1100SC	P11C	90	130	4	800	2.2	150	30	50
CLP1300SC/S-CLP1300SC	P13C	120	160	4	800	2.2	150	25	45
CLP1500SC/S-CLP1500SC	P15C	140	180	4	800	2.2	150	25	40
CLP1800SC/S-CLP1800SC	P18C	170	220	4	800	2.2	150	25	35
CLP2000SC/S-CLP2000SC	P20C	180	220	4	800	2.2	150	20	35
CLP2300SC/S-CLP2300SC	P23C	190	260	4	800	2.2	150	25	35
CLP2600SC/S-CLP2600SC	P26C	220	300	4	800	2.2	150	20	35
CLP3100SC/S-CLP3100SC	P31C	275	350	4	800	2.2	150	20	35
CLP3500SC/S-CLP3500SC	P35C	320	400	4	800	2.2	150	20	35
CLP4000SC/S-CLP4000SC	P40C	360	460	4	800	2.2	150	20	35
CLP4500SC/S-CLP4500SC	P45C	400	540	4	800	2.2	150	20	35
CLP5000SC/S-CLP5000SC	P50C	440	600	4	800	2.2	150	20	35

Notes:

- Absolute maximum ratings measured at $T_A = 25^\circ C$ (unless otherwise noted) .
- Devices are bi-directional.

3. Surge Ratings

Series	I _{PP}					V _{PP}	I _{TSM}	di/dt
	2/10 μ S ¹	8/20 μ S ¹	10/160 μ S ¹	10/560 μ S ¹	10/1000 μ S ¹	5/310 μ S ¹	50/60	
	2/10 μ S ²	1.2/50 μ S ²	10/160 μ S ²	10/560 μ S ²	10/1000 μ S ²	10/700 μ S ²	Hz	
	A min	A min	A min	A min	A min	KV min	A min	Amps/ μ s max
C	500	400	200	150	100	4	30	500

Notes: – Peak pulse current rating (IPP) is repetitive and guaranteed for the life of the product.

1. Current waveform in μ s – IPP ratings applicable overtemperature range of -40°C to +85°C
2. Voltage waveform in μ s – The device must initially be in thermal equilibrium with -40°C < T_J < +150°C

4. Thermal Considerations

Package	Symbol	Parameter	Value	Unit
DO-214AA	T _J	Operating Junction Temperature Range	-40 to +150	°C
	T _S	Storage Temperature Range	-40 to +150	°C
	R _{θJA}	Thermal Resistance: Junction to Ambient	160	°C/W

5.ELECTRICAL CHARACTERISTICS CURVES

Figure 1 - V-I Characteristics

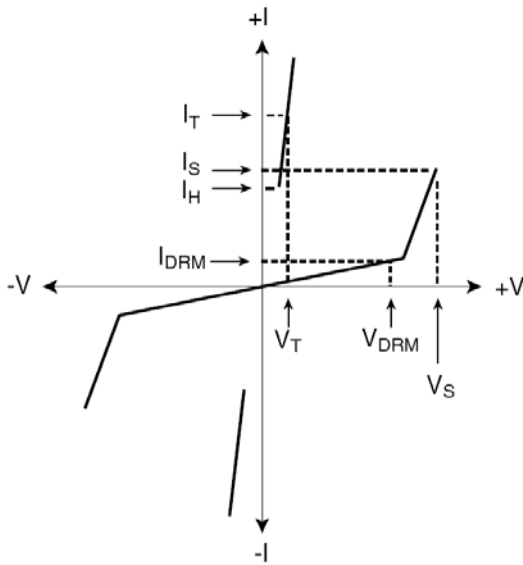


Figure 2 - $t_r \times t_d$ Pulse Waveform

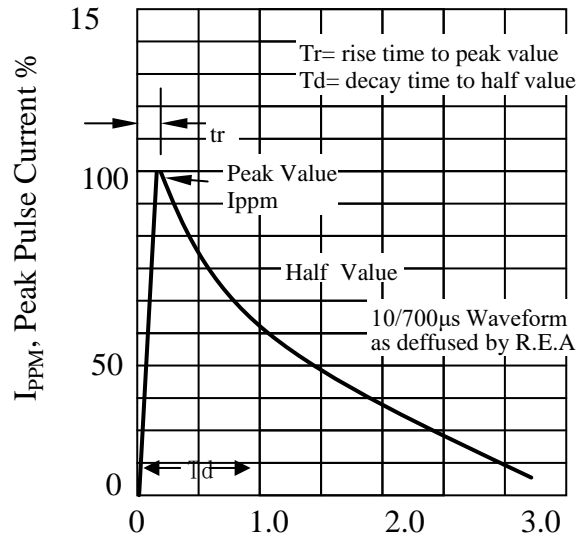


Figure 3 - Normalized V_S Change vs. Junction Temperature

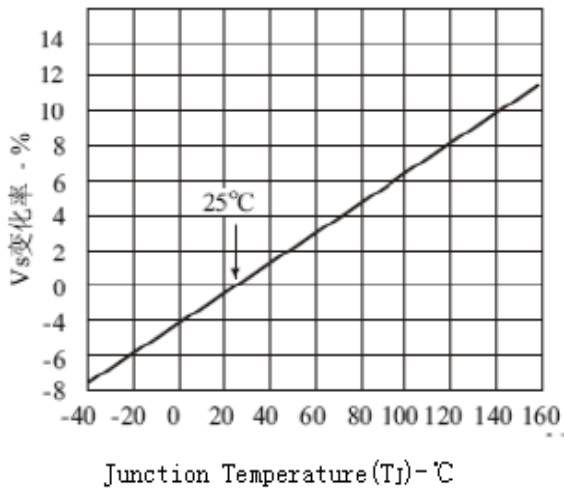
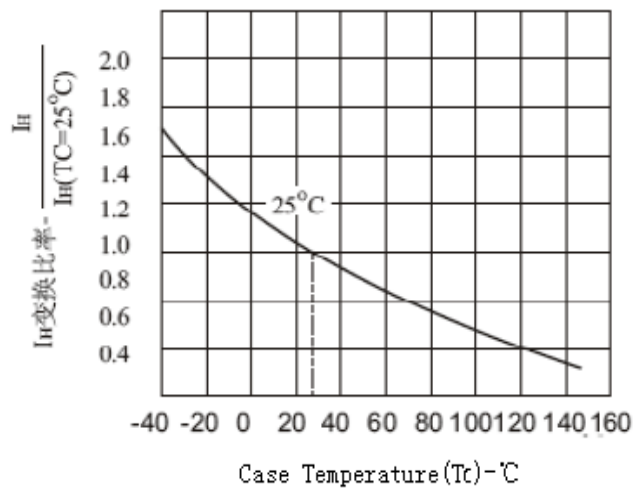
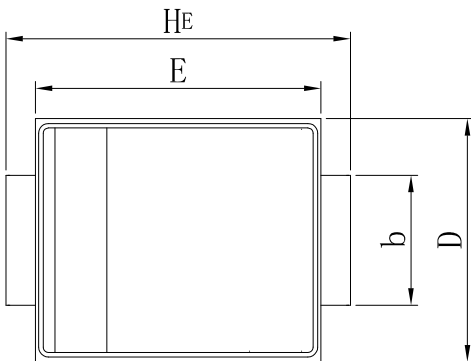
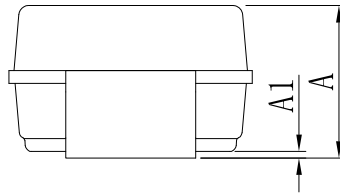
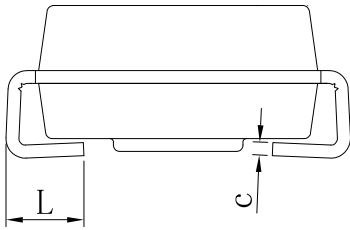


Figure 4 - Normalized DC Holding Current vs. Case Temperature

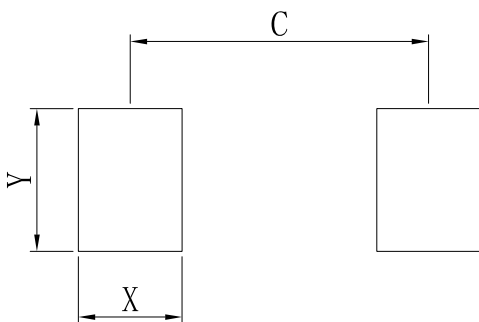


6. OUTLINE AND DIMENSIONS



CSMB			
DIM	MIN	TYP	MAX
A	2.20	2.35	2.50
A1	0.05	0.10	0.20
b	1.80	2.00	2.20
c	0.10	0.20	0.30
D	3.30	3.75	3.94
E	4.06	4.40	4.60
HE	5.20	5.31	5.45
L	0.90	1.30	1.60
All Dimensions in mm			

7. SOLDERING FOOTPRINT



CSMB	
DIM	(mm)
X	1.60
Y	2.20
C	4.60

DISCLAIMER

- Before you use our Products, you are requested to carefully read this document and fully understand its contents. LRC shall not be in any way responsible or liable for failure, malfunction or accident arising from the use of any LRC's Products against warning, caution or note contained in this document.
- All information contained in this document is current as of the issuing date and subject to change without any prior notice. Before purchasing or using LRC's Products, please confirm the latest information with a LRC sales representative.